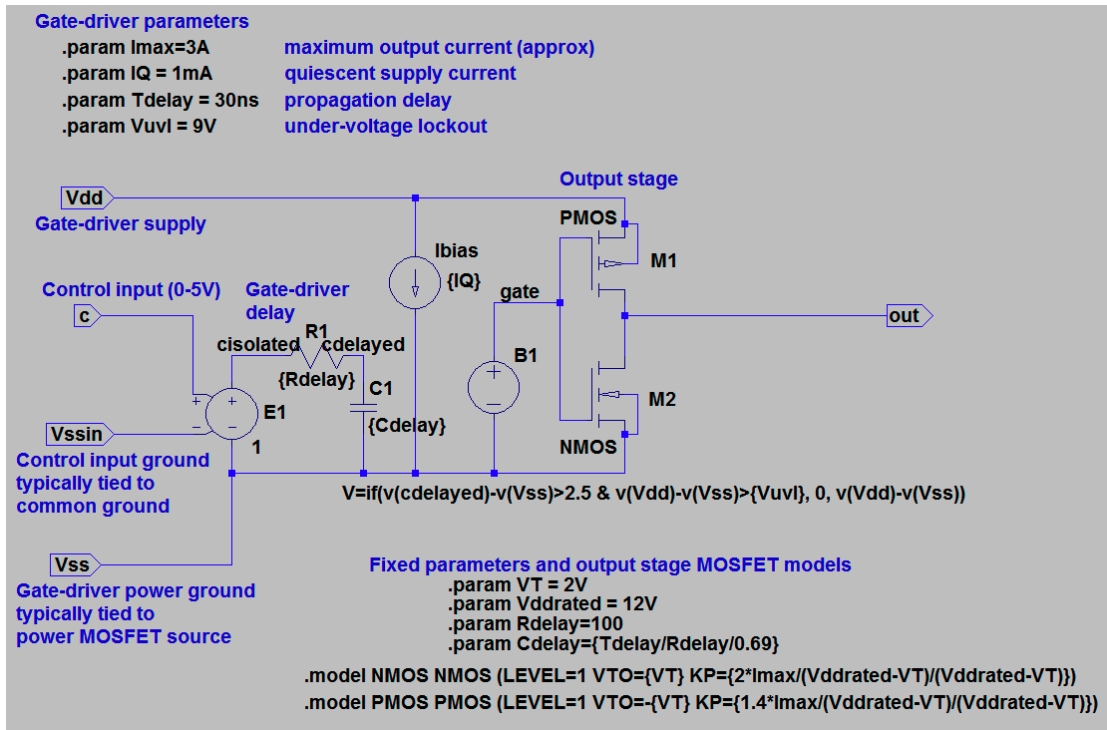# What is in the Behavioral Gate Driver Model

- Voltage-dependent source E1 provides isolation of input and output signals
- R1 and C1 add propagation delay
- Ibias models quiescent current of driver circuit
- B1 is a dependent voltage source having a logic output with 2.5 V input threshold

- PMOS and NMOS model the output stage of the driver. The I-V characteristics of these transistors are chosen according to Imax, the rated current of the driver.
- Undervoltage lockout function: the output is low when Vdd < Vuvl